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Agenda

Performance tuning concepts Using the sampling collector How sampling works Sampling Over Time Call Graph

VTune™ Performance Analyzer

- Collecting performance data from the system running your application
- Organizing and displaying the data in a variety of interactive views, from system-wide down to source code or processor instruction perspective.
- Identifying potential performance issues and suggesting improvements.

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Supported Environments

Local and remote data collection

Profile applications that are running on the system that has the analyzer installed on it, or

Run profiling experiments on other systems that are running VTune analyzer remote agents on them

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Local Performance Analysis

- Microsoft Windows* operating systems
 Red Hat Linux*

- Microsoft Windows operating systems
 Red Hat Linux

- For specific operating systems versions, see the release notes

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Host/Target Environment $\mathsf{VTune}^{\texttt{IM}}$ Performance Analyzer supports remote data collection VTune[™] Performance Analyzer installed on host system Remote agent installed on target system Target System **Host System** •IA-32 or Itanium® •Windows*

LAN Connection

operating system Controls target

•View results of data collection

processor family •Windows or Linux* •Intel[®] PXA2xx processors running Windows CE* (intel)

Feature Overview Sampling Call graph (intel)









What Is a Hotspot?

Where in an application or system there is a significant amount

- Where = address in memory => OS process => OS thread => executable file or module => user function (requires symbols) => line of source code (requires symbols with line numbers) or processor (assembly) instruction
- Significant = activity that occurs infrequently probably does not have much impact on system performance
- Examples of other events: Cache misses, branch mispredictions, floating-point instructions retired, partial register stalls, and so on.
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Sampling: The Statistical Method of Finding Hotspots

The sampling collector

- Time-based
 Event-based: Triggered by the occurrence of a certain number of microarchitectural events

VTune™ Analyzer Sampling Collector

- Execution address in memory (CS:IP)
 Operating system process and thread ID
 Executable module loaded at that address
- If you have symbols for the module, post-processing can identify the function or method at the memory address.
 Line numbers from the symbol file can direct you to the relevant line of source code.

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Sampling Collector

Periodically interrupt the processor to obtain the execution context

- Time-based sampling (TBS) is triggered by:
- Operating system timer services
 Every n processor clockticks
- Event-based sampling (EBS) is triggered by processor event counter

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These events are processor-specific, like L2 cache misses, branch mispredictions, floating-point instructions retired, and so on

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Three Key Benefits of Sampling

You do not have to modify your code.

- But DO compile/link with symbols and line numbers.
- But DO make release builds with optimizations.

Sampling is system-wide.

- Not just YOUR application.
- You can see activity in operating system code, including drivers.
- Sampling overhead is very low.Validity is highest when perturbation is low.
- Overhead can be reduced further by turning off progress meters in the user interface.

How else can you reduce sampling overhead?

	Basics of VTune TH Performance Analyzer	
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How Many Samples Are Enough?

- One million samples for a five-second run?
- Do you have enough samples for it to be statistically significant?
- How much overhead are you causing?
- What if you only get 100 samples?
- Is your sample after number 1?
- Are you getting a good profile?

About 1,000 samples per second is a good balance between significance and overhead

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Objective: 1,000 Samples Per Second

- What is the sample after value for clockticks
- Dependent upon CPU clock speed
- ANSWER: CPU clock speed in KHz
- If CPU clock speed = 1,400,000,000 Hz
 Sample after 1,400,000 clockticks
- Sample after 1,400,000 clockticks

What is the sample after value for L2 cache read misses?

- It depends on how often you miss the L2 cache!
 Circular definition? Is not that what you are trying to determine?
- Make an intelligent guess! Estimate!
- More or less often than the clockticks?10 times? 100 times? 1000 times?



Calibration

Sets the sample after value to get a reasonable number of samples.

• ~ 1000 samples per second per logical CPU

Requires the workload to be run twice

Manual Calibration:

- Uncheck Calibrate Sample After value
 Found on Advanced Activity Configuration dialog
- Start with default value or an estimate
- Run a test
- Modify the sample after value and re-test
- Try to get about a 1000 samples per second per logical CPU

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Sampling Over Time

Shows how sample distributions change over time by process, thread, or module

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Zoom in on time regions

Useful fo

- Identifying time-variant performance characteristics
- Understanding thread behavior

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Sampling Over Time Usage Model

Collect sampling data

Select items of interest from either the process, thread, or modules view Click # Highlight region of interest

Click 😫

Click $\,\,{}^{\bigstar}\,$ to see process/thread/address histogram for time region

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Activity 2: Sampling Over Time

 Learn how to use the Sampling Over Time view

 State of Views[®] Performance Analyser

Call Graph Profiling

Tracks the function entry and exit points of your code at run time

Uses binary instrumentation

Uses this data to determine program flow, critical functions and call sequences

Not system-wide: Only profiles code in applications call path in Ring 3

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What Can You Profile?

Win32 applications Stand-alone Win32* DLLs Stand-alone COM+ DLLs Java applications .NET* applications ASP.NET applications Linux32* applications

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Performance Metric	Description
Self Time	Total time in a function, excluding time spent in its children (includes wait time)
Total Time	Time measured from a function entry to exit point
Total Wait Time	Time spent in a function and its children when the thread is blocked
Wait Time	Time spent in a function when the thread is blocked (excludes blocked time in its children)
Calls	Number of times the function is called

Activity 3: Call Graph



ampling	Call graph
.ow overhead	Higher overhead
System-wide	Ring 3 only on your application call tree
System-wide address histogram	Show function level hierarchy with call counts, times, and the critical path
For function level drill-down, must have debug information	Must re-link with /fixed:no, automatically instruments
Can sample based on time and other processor events	Results are based on time

Java* and .NET* Applications

Provides performance data for both managed code and unmanaged code

Gives insight into how managed code calls translate into $\rm Win32^{\ast}$ calls

Uses managed code profiling API and binary instrumentation

Basics of VTuneTM Performance Analyzer

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Intel® Tuning Assistant

Identifies bottlenecks in:

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Pentium® 4, Pentium M®, Itanium® 2, and Pentium® III
 processors.

Uses EBS and Counter Monitor data.

Shows scaling differences between different runs.

Code Coach is still available but is not enabled by default.







Lab Activity 3: Getting Tuning Advice

Learn how to get processor-specific tuning advice



Windows* Command Line Interface

Collect sampling data from the command line. Useful for integrating performance data collection into your automated regression testing. View the data in the VTune[™] Performance Analyzer or export as ASCII text.

Invoke by typing "vtl" at the command line.



Windows* Command Line Interface

Creates hidden project structure

- To create an activity: vtl create [activity name] + options
- To run an activity: vtl run [activity name]
- To view activities type: vtl show

To view results of a particular activity type: vtl view [activityname::result] [options]

To delete the entire project: vtl delete -all

To delete a specific activity: vtl delete <activity name>

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Windows* Command Line Interface Examples

Sample on clockticks and instructions retired and launch app matrix.exe:

vtl activity a1 -c sampling -app matrix.exe run See the clocktick hotspots in matrix.exe:

- vtl view al::r1 -hf -mn matrix.exe
- See the number of samples in each module system wide:

vtl al::r1 view -modules

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Windows* Command Line Interface Help

For general command line arguments: vtl -help

For sampling command line arguments and events:

vtl -help -c sampling

For in depth help and examples go to: Start->Programs->Intel® VTuneTM Performance Analyzer->Help for the Command Line

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Lab Activity 4: Using the Windows* Command Line Interface Learn how to collect sampling data from the command line

Basics of VTune— Performance Analyzer



Set instrumentation levels.

- Helps control overhead
- Select which functions are instrumented.
- Helps control overhead

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Call Graph Advanced Options



Instrumentation Level	Description	Debug Info Required?
All Functions	Every function in the module is instrumented.	Yes
Custom	You can specify which functions are instrumented	Yes
Export	Every function in the module's export table is instrumented.	No
Minimal	The module is instrumented but no data is collected for it.	No



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Lab Activity 6: Using Sampling and Call Graph Together

Optimize an application (linpack) using sampling and call graph



Sampling and Call Graph Have Different Hotspots?

Self time includes blocked time.

Event-based sampling (EBS) and time-based sampling (TBS) do not include blocked time in functions (this usually appears in processor.sys).

Hotspots should be the same for self time – wait time (this is non-blocked self time).



What Counter Monitor Does

Collects hardware and software performance counter data

- Correlate counter data with sampling data



Performance DLL SDK

SDK for creating custom performance counters that can be

Available on the Intel® web site

Example: performance counter that measures the transactions per second for a server application

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Performance DLL SDK

SDK for creating custom performance counters that can be used by counter monitor Example: performance counter that measures the transactions per second for a server application (intel)





Lab Activity 7: Counter Monitor Use counter monitor to analyze gzip

Trigger API

Allows you to create your own mechanism to programmatically trigger performance counter data collection

Example: collect counter monitor data every time a frame is rendered