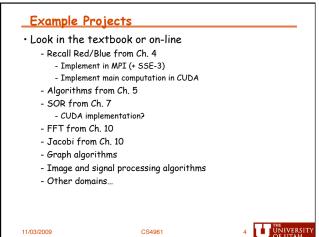
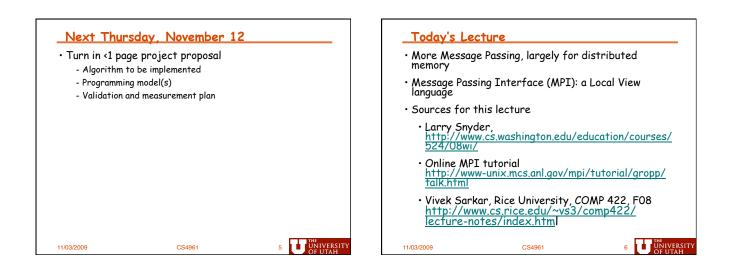
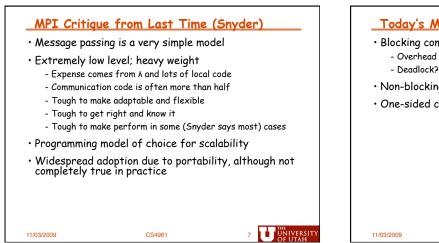


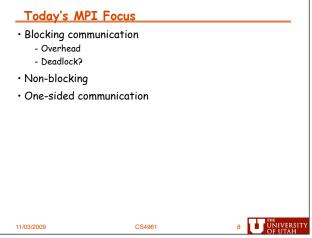
<ul> <li>Purpose:</li> </ul>			۰L
- A chance to and explore	dig in deeper into a parallel p concepts.	programming model	· L
- Present resu	ilts to work on communication	n of technical ideas	
<ul> <li>Write a non-t two parallel p cases, just do</li> </ul>	rivial parallel program tl rogramming languages/m two separate implemen	nat combines 10dels. In some tations.	
- OpenMP + S			
- TBB + SSE-	3		
- MPI + Open	MP		
- MPI + SSE-	3		
- MPI + CUDA			
- Open CL???	(keep it simple! need backup	p plan)	
<ul> <li>Present resultion class</li> </ul>	ts in a poster session on	the last day of	
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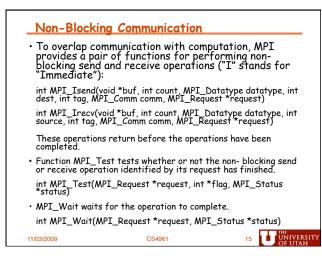


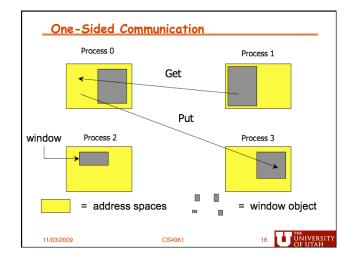
MPI is a message-passing library interface standard.	<ul> <li>Same process of definition by MPI Forum</li> </ul>		
- Specification, not implementation - Library, not a language	<ul> <li>MPI-2 is an extension of MPI - Extends the message- passing model.</li> <li>Parallel I/O</li> </ul>		
- Classical message-passing programming model			
• MPI was defined (1994) by a broadly-based group of parallel computer vendors, computer scientists, and applications developers.	Remote memory operations (one-sided)		
applications developers. - 2-year intensive process	<ul> <li>Dynamic process management         <ul> <li>Adds other functionality</li> <li>C++ and Fortran 90 bindings</li> <li>similar to original C and Fortran-77 bindings</li> <li>External interfaces</li> </ul> </li> </ul>		
Implementations appeared quickly and now MPI is taken for granted as vendor-supported software on any parallel machine.			
Free, portable implementations exist for clusters and other environments (MPICH2, Open MPI)	- Language interoperability - MPI interaction with threads		
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Non-Buffer	ed vs. Buffered S	Sends		Non-Block
<ul> <li>A simple meth is for the sen safe to do so.</li> </ul>	od for forcing send/r d operation to return	eceive semantics only when it is		<ul> <li>The program and receive.</li> </ul>
<ul> <li>In the non-buddees not return encountered of</li> </ul>	iffered blocking send, rn until the matching r it the receiving proces	, the operation receive has been ss.		<ul> <li>This class or send or rece safe to do s</li> </ul>
	dlocks are major issue king sends.			• Non-blockin a check-sta • When used
<ul> <li>In buffered blocking sends, the sender simply copies the data into the designated buffer and returns after the copy operation has been completed. The data is copied at a buffer at the receiving end as well.</li> </ul>				• Message par blocking and
<ul> <li>Buffering alle overheads.</li> </ul>	viates idling at the ex	pense of copying		
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<ul> <li>The programm and receive.</li> </ul>	her must ensure sem	antics of the send
<ul> <li>This class of a send or receive safe to do so.</li> </ul>	non-blocking protoco ve operation before i	ls returns from the it is semantically
<ul> <li>Non-blocking a check-statu</li> </ul>	operations are gener s operation.	ally accompanied by
<ul> <li>When used co overlapping co computations.</li> </ul>	rrectly, these primi mmunication overhe	tives are capable of ads with useful
<ul> <li>Message pass blocking and n</li> </ul>	ing libraries typically on-blocking primitiv	y provide both es.

Deadlock?	Deadlock?			
int a[10], b[10], myrank; MPI_Status status; MPI_Comm_rank(MPI_COMM_WORLD, &myrank);	Consider the following piece of code, in which process i sends a message to process <i>i</i> + 1 (modulo the number of processes) and receives a message from process <i>i</i> - 1 (module the number of processes).			
<pre>if (myrank == 0) {     MPI_Send(a, 10, MPI_INT, 1, 1, MPI_COMM_WORLD);     MPI_Send(b, 10, MPI_INT, 1, 2, MPI_COMM_WORLD); } else if (myrank == 1) {     MPI_Recv(b, 10, MPI_INT, 0, 2, MPI_COMM_WORLD);     MPI_Recv(a, 10, MPI_INT, 0, 1, MPI_COMM_WORLD);     }</pre>	int a[10], b[10], npes, myrank; MPI_Status status; MPI_Comm_size(MPI_COMM_WORLD, &npes); MPI_Comm_rank(MPI_COMM_WORLD, &myrank); MPI_Send(a, 10, MPI_INT, (myrank+1)%npes, 1, MPI_COMM_WORLD); MPI_Recv(b, 10, MPI_INT, (myrank-1+npes)%npes, 1, MPI_COMM_WORLD);			
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MPI One-S Memory Ac	Sided Communicati cess (RMA)	ion or Rem	ote		tructs supporting C ation (RMA)	One-Sided
- Balancing et architectur	-2 RMA Design ficiency and portability a es nory multiprocessors	cross a wide cl	ass of	- Collective	reate exposes local m y other processes in c operation vindow object	emory to RMA a communicator
- NUMA arch - distributed - Workstatio	-memory MPP's, clusters			• MPI_Win_f	ree deallocates windo	w object
• Retaining "loc	ok and feel" of MPI-1			• MPI_Put me memory	oves data from local n	nemory to remote
<ul> <li>Dealing with subtle memory behavior issues: cache coherence, sequential consistency</li> </ul>			• MPI_Get retrieves data from remote memory into local memory			
				• MPI_Accum local values	ulate updates remote	memory using
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