

Homework	2:	Mapping	to	Architecture

Due before class, Thursday, September 6

Objective: Begin thinking about architecture mapping issues

Turn in electronically on the CADE machines using the handin program: "handin cs4230 hw2 <probfiles"

Problem 1: (2.3 in text) [Locality]

Problem 2: (2.8 in text) [Caches and multithreading]

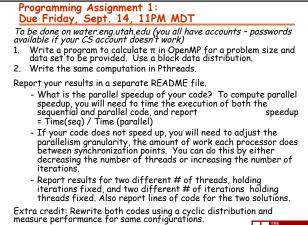
- Problem 3: [Amdahl's Law] A multiprocessor consists of 100 processors, each capable of a peak execution rate of 20 Gflops. What is performance of the system as measured in Gflops when 20% of the code is sequential and 80% is parallelizable?
- Problem 4: (2.16 in text) [Parallelization scaling]
- Problem 4: (2.10 in text) [rai alteration scaling]
 Problem 5: [Buses and crossbars] Suppose you have a computation that uses two vector inputs to compute a vector output, where each vector is stored in consecutive memory locations. Each input and output location is unique, but data is loaded/stored from cache in 4-word transfers. Suppose you have P processors and N data elements, and execution time is a function of time L for a load from memory and time C for the computation. Compare parallel execution time for a shared memory architecture with a bus (Nehalem) versus a full crossbar (Niagara) from Lecture 3, assuming a write back cache that is larger than the data footprint.

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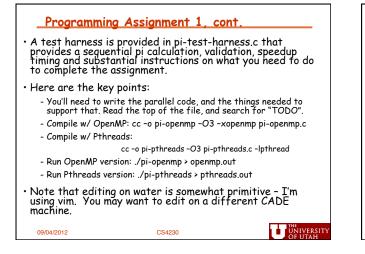
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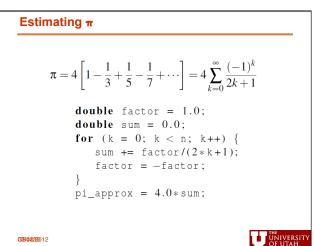
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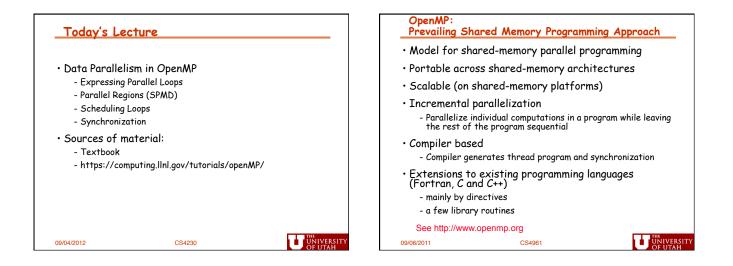


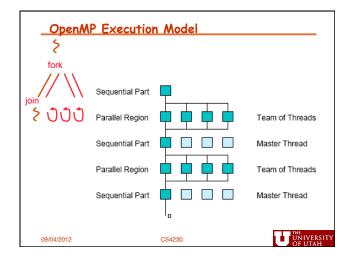
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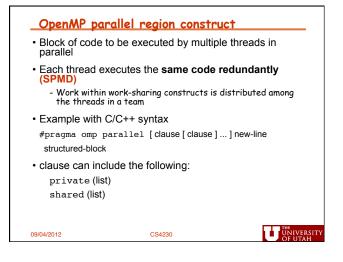
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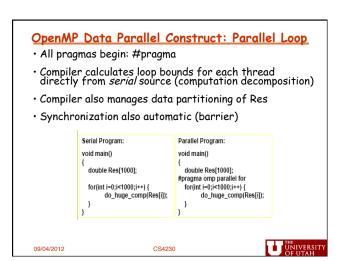


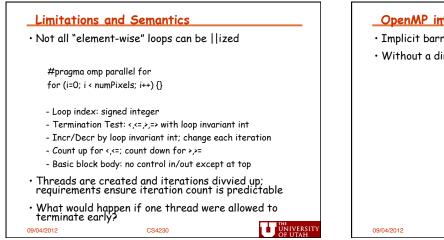


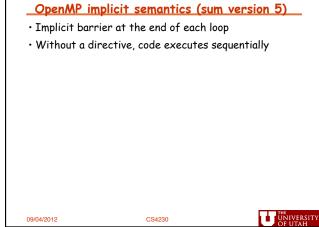




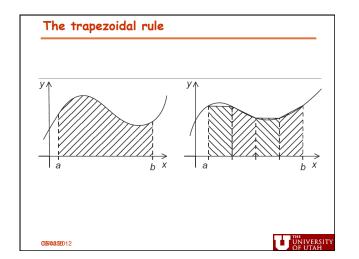
Programming Model -	Data Sharing
 Parallel programs often employ two types of data Shared data, visible to all 	<pre>// shared, globals int bigdata[1024];</pre>
threads, similarly named - Private data, visible to a single thread (often stack-allocated)	<pre>void* foo(void* bar) {</pre>
 PThreads: Global-scoped variables are shared Stack-allocated variables are private 	<pre>int tid; #pragma omp parallel \ shared (bigdata) \</pre>
 OpenMP: shared variables are shared private variables are private Default is shared Loop index is private 	<pre>private (tid) { /* Calc. here */ } }</pre>
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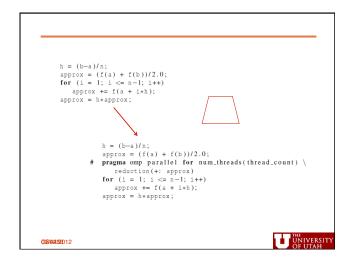


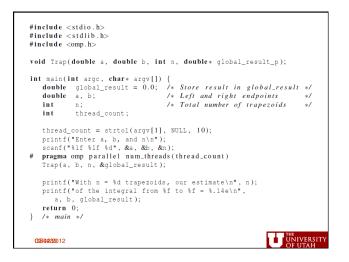


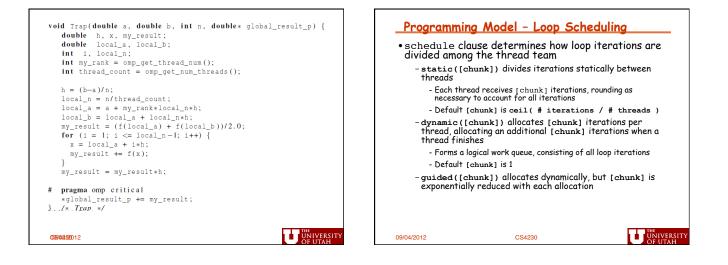
	_OpenMp Reductions
• Enclosed code	 OpenMP has reduce operation
- executed by all threads, but	sum = 0;
- restricted to only one thread at a time #pragma omp critical [(name)] new-line	<pre>#pragma omp parallel for reduction(+:sum) for (i=0; i < 100; i++) { sum += array[i]; </pre>
structured-block	}
 A thread waits at the beginning of a critical region until no other thread in the team is executing a critical region with the same name. All unnamed critical directives map to the same unspecified name. 	 Reduce ops and init() values (C and C++): + 0 bitwise & ~0 logical & 1 - 0 bitwise 0 logical 0 * 1 bitwise ^ 0
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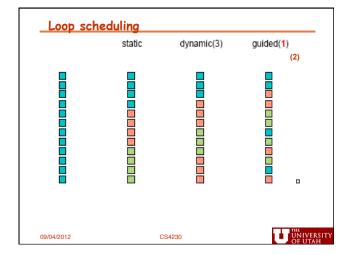


<u>Serial</u>	algorithm
	<pre>/* Input: a, b, n */ h = (b-a)/n; approx = (f(a) + f(b))/2.0; for (i = 1; i <= n-1; i++) { x_i = a + i*h; approx += f(x_i); } approx = h*approx;</pre>
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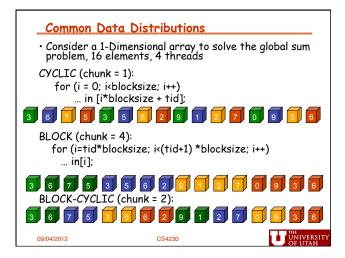


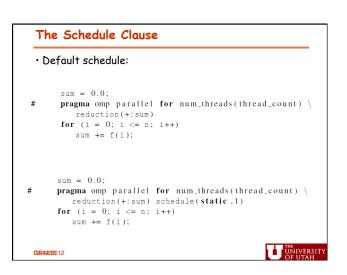


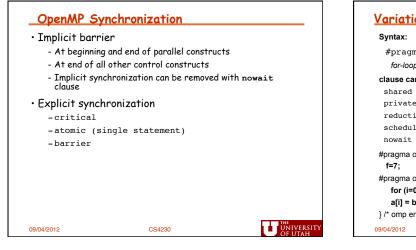


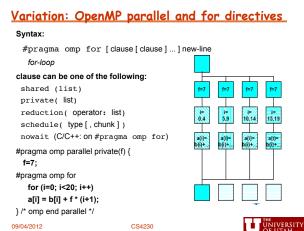
•	<u>cheduling attribut</u>	
• RUNTIME Th runtime by th OMP_SCHED for this claus	e scheduling decision e environment variabl ULE. It is illegal to sp e.	is deferred until e ecify a chunk size
• AUTO The sc compiler and/	heduling decision is de or runtime system.	elegated to the
• NO WAIT / not synchroni	nowait : If specified, ze at the end of the p	then threads do parallel loop.
• ORDERED: Sp must be exect program.	pecifies that the iterc uted as they would be	itions of the loop in a serial
• COLLAPSE: S should be coll divided accorr order corresp	pecifies how many loo apsed into one large it ding to the schedule c onds to original seque	ps in a nested loop teration space and lause (collapsed ential order).
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Impact of Scheduling Decision	A Few Words About Data Distribution
 Load balance Same work in each iteration? Processors working at same speed? Scheduling overhead Static decisions are cheap because they require no run-time coordination Dynamic decisions have overhead that is impacted by complexity and frequency of decisions Data locality Particularly within cache lines for small chunk sizes Also impacts data reuse on same processor 	 Data distribution describes how global data is partitioned across processors. Recall the CTA model and the notion that a portion of the global address space is physically co-located with each processor This data partitioning is implicit in OpenMP and may not match loop iteration scheduling Compiler will try to do the right thing with static scheduling specifications
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OpenMP environmen	t variables	<u>Summary of Lectur</u>	'e
OMP_NUM_THREADS Sets the number of threads to	use during execution	• OpenMP, data-parallel con - Task-parallel constructs lo	
, ,	the number of threads is enabled, the able is the maximum number of	 What's good? Small changes are required sequential (parallel formula 	
setenv OMP_NUM_TH	READS 16 [csh, tcsh] READS=16 [sh, ksh, bash]	- Avoid having to express lo - Portable and scalable, corr	
 OMP_SCHEDULE applies only to do/for and p have the schedule type RUNT: 	arallel do/for directives that	 What is missing? Not completely natural if v scratch 	want to
 sets schedule type and chunk 		 Not always possible to exp constructs 	oress c
 For example, 		- Locality management	
setenv OMP_SCHEDU	LE GUIDED,4 [csh, tcsh]	- Control of performance	
export OMP_SCHEDU	LE= GUIDED,4 [sh, ksh, bash]		
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- tructs only
 - o produce a parallel program from on)
 - evel mapping details
 - on 1 processor
 - nt to write a parallel code from
 - ss certain common parallel

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