Operand Practice

CPU

register	value
%eax	0x2
%ecx	0x304
%esi	0x4
%edi	0x30C

Memory

address	value
0x300	0xA
0x304	0xB
0x308	0xC
0x30C	0xD

operand	meaning	value
0x300	M[0x300]	0xA = 10
%eax	R[%eax]	0x2 = 2
(%ecx)	M[R[%ecx]] = M[0x304]	0xB = 11
(%esi,%ecx)	M[R[%esi]+R[%ecx]] = M[0x4+0x304] = M[0x308]	0xC = 12

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CPU

register	value
%eax	0x2
%ecx	0x304
%esi	0x4
%edi	0x30C

Memory

address	value
0x300	0xA
0x304	0xB
0x308	0xC
0x30C	0xD

operand	meaning	value
$0x300	0x300	0x300 = 768
0x300(%esi)	M[0x300+0x4] = M[0x300+0x4] = M[0x304]	0xB = 11
0x300(%esi,%eax,4)	M[0x300+0x4+R[%eax]×4] = M[0x300+0x4+0x2×4] = M[0x30C]	0xD = 13