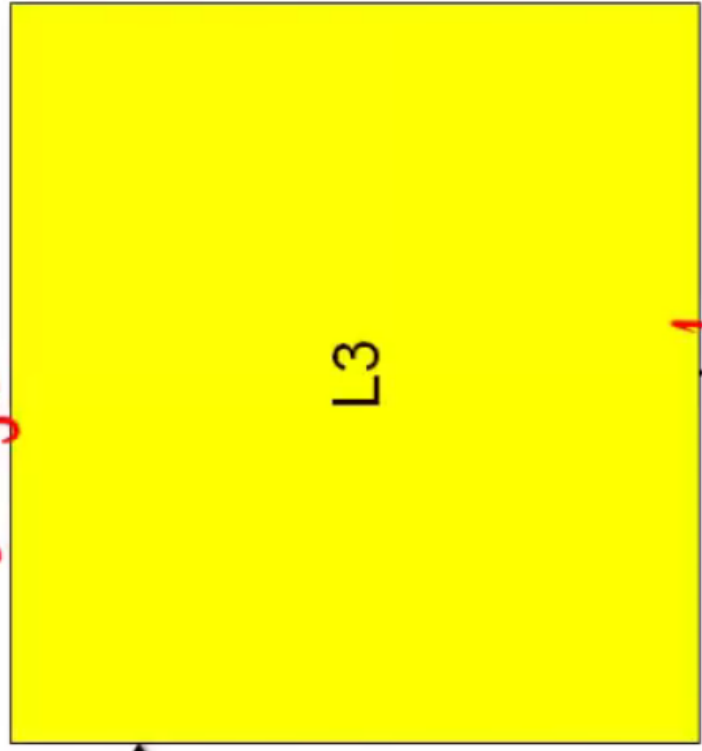


# The Cache Hierarchy

1000 incls → 400 w/ST  
20

incl  
excl  
5 MPKI  
20 MPKI

95% hits  
32KB  
1-cyc  
50%  
256KB  
10-cyc  
2MB  
30-cyc  
non-incl



Spatial X+1 at time t  
Temporal X at time t  
X at time t+i

Main Memory



Off-chip memory

300 cycle access

4GB