

# Memory Dependence Checking

Ld	0x abcdef	✓
Ld		
St	<del>0x abcdef</del>	
Ld		
Ld	0x abcdef	✗
St	0x abcd00	
Ld	0x abc000	✓
Ld	0x abcd00	✓

- The issue queue checks for register dependences and executes instructions as soon as registers are ready
- Loads/stores access memory as well – must check for RAW, WAW, and WAR hazards for memory as well
- Hence, first check for register dependences to compute effective addresses; then check for memory dependences