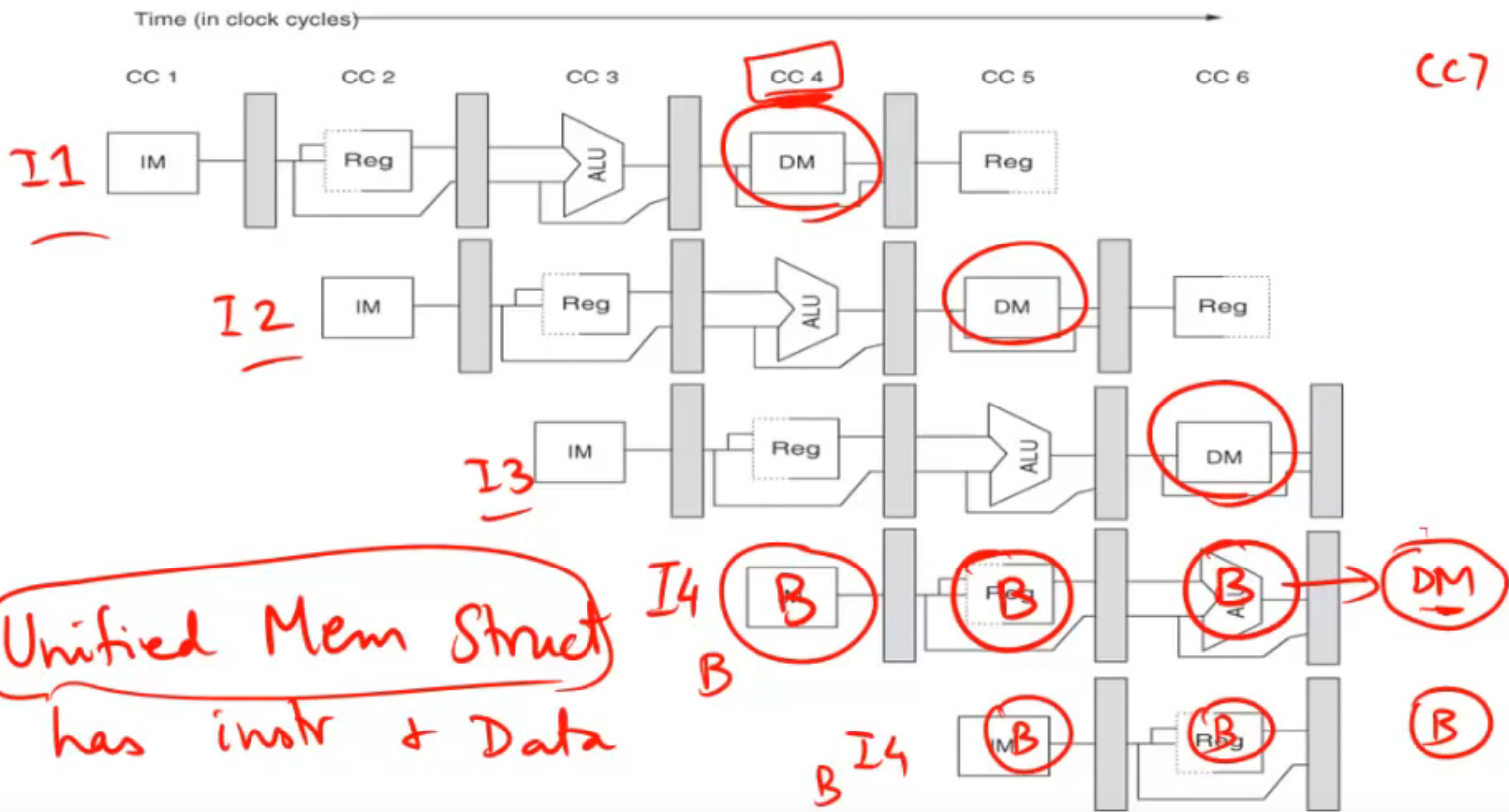


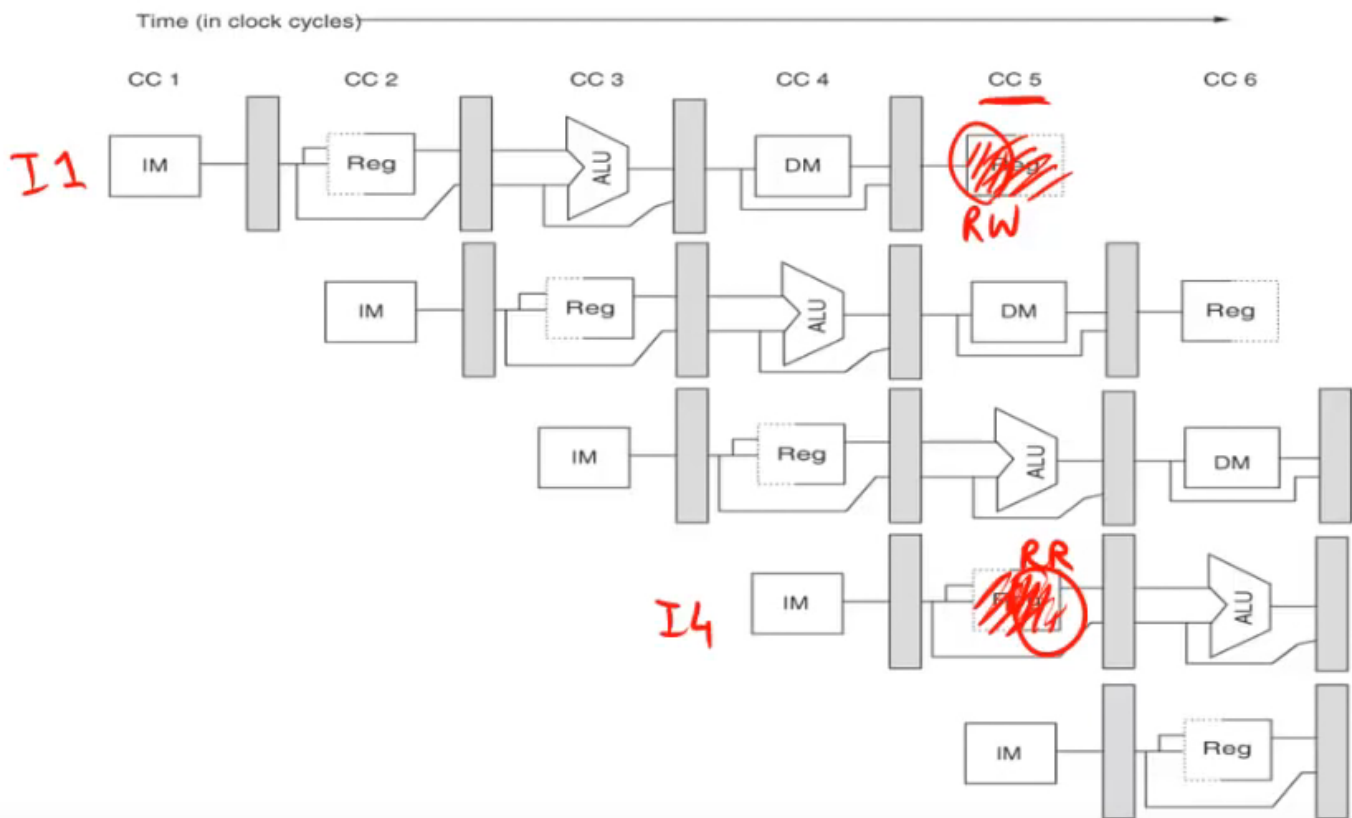
A 5-Stage Pipeline

IPC of 1 \rightarrow 0.5

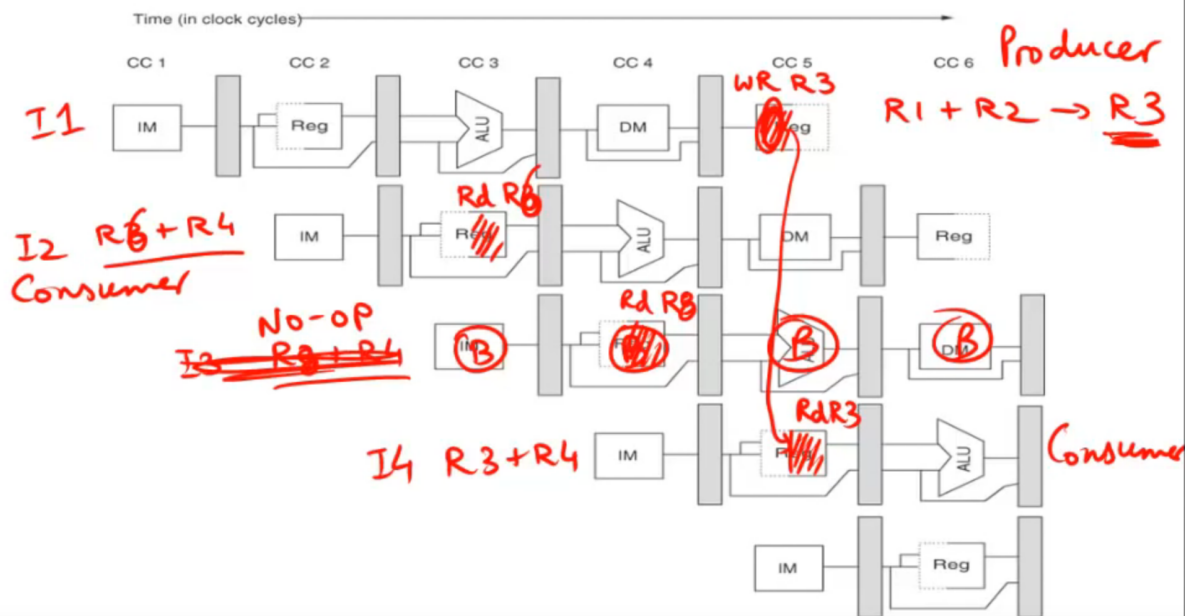


Unified Mem Struct
has instr & Data

A 5-Stage Pipeline



A 5-Stage Pipeline



A 5-Stage Pipeline

