

Layers

Weak: Parity

A 1-bit checksum is a **parity** check

... since that I bit is either on/odd or off/even

10001010	\rightarrow	1
10011010	\rightarrow	0
10111010	\rightarrow	1

Fast, but two corrupt bits cancel, which is especially bad when corruption is bursty

Better: Checksum

Adding numbers and keeping low bits is better

10001010		1 111 1010
10011110		10011110
10111010		10111010
10111110		10111110
10100000	≠	00010000

But it can miss many kinds of regular patterns

10001010		10001 1 10
10011110		10011 <mark>0</mark> 10
10111010		10111 <mark>1</mark> 10
10111110		10111 <mark>0</mark> 10
10100000	=	10100000

Division is much better at mixing numbers

2871088
43)123456789
86
374
344
305
301
46
43
37
0
378
344
349
344
5

Division is much better at mixing numbers

2871088 43)123456789	287 <mark>3413</mark> 43)123 5 56789
86	86
374	37 <mark>5</mark>
344	344
305	315
301	301
46	146
43	129
37	177
0	172
378	058
344	043
349	159
344	129
5	30

Division is much better at mixing numbers

2871088	2873413
43)123456789	43)123 <mark>5</mark> 56789
86	86
374	375
344	344
305	315
301	301
46	146
43	129
37	177
0	172
378	058
344	043
349	159
344	129
5	30

General division is expensive on a CPU Specialized division can be fast, especially in hardware

A cyclic redundancy check (CRC) takes advantage of division:

$$R = \text{remainder of } \frac{D \times 2^r}{G}$$

$$d$$
 = number of bits to check

$$D = d$$
 bits of data

- r = bits for hash result (typically 8, 12, 16, or 32)
- G = a carefully chosen, agreed-on r+1-bit number

$$R =$$
 the result for D

For r = 32, IEEE standard is $G = 0 \times 104C11DB7$

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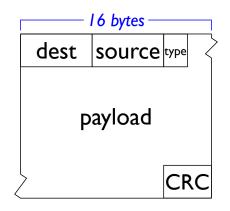
$$D = d$$
 bits of data

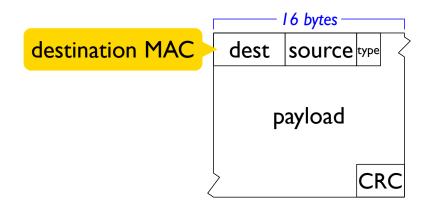
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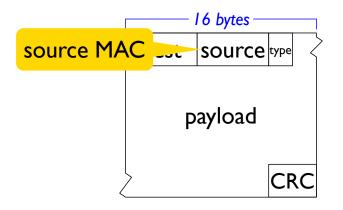
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 the result for D

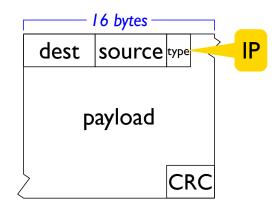
For r = 32, IEEE standard is $G = 0 \times 104 \text{C11DB7}$

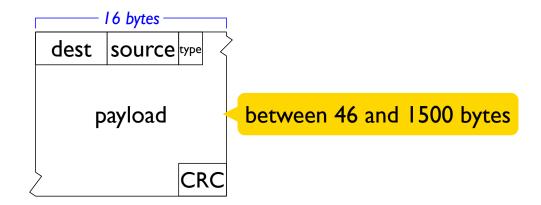
Detects any *r*-bit error burst

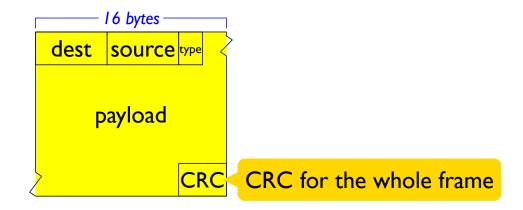




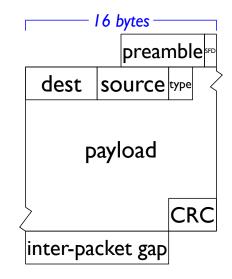




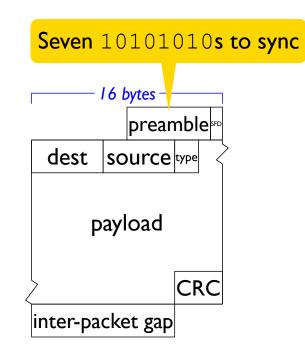




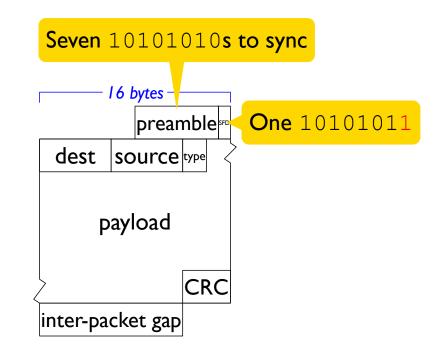
Ethernet Physical Layer Layout



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Ethernet Physical Layer

Originally, machines on an ethernet LAN shared a wire

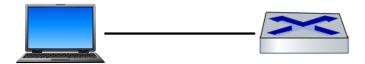
Modern ethernet is always **switched**: there's a dedicated wire from each machine to the switch

This makes MAC addresses somewhat redundant!

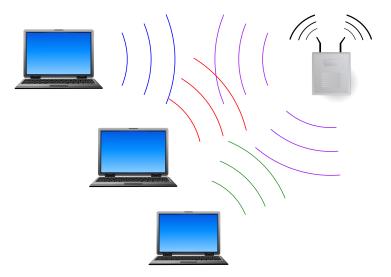
Many ethernet frames are not physically ethernet at all, because the easiest way to create a new physical layer is to emulate an ethernet device

Coordinating Communication

Easy mode: point-to-point communication

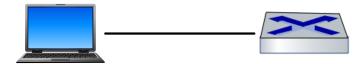


Hard mode: shared communication medium

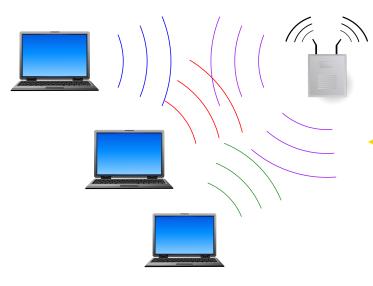


Coordinating Communication

Easy mode: point-to-point communication



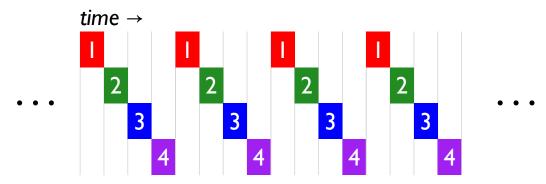
Hard mode: shared communication medium



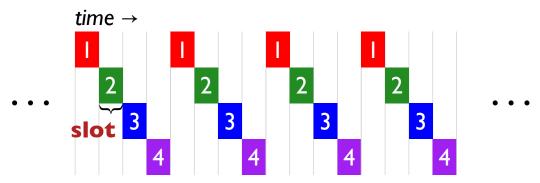
Goals:

- divide bandwidth fairly
- only one active
 - \Rightarrow gets full bandwidth

Time-division multiplexing (TDM):



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Time-division multiplexing (TDM):

3

slot



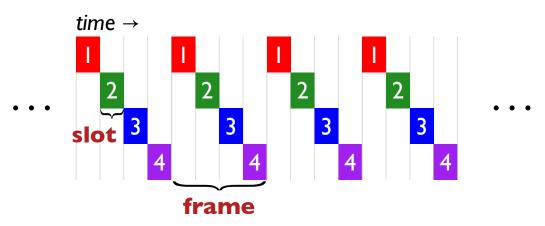
4

3

3

Frequency-division multiplexing (FDM):

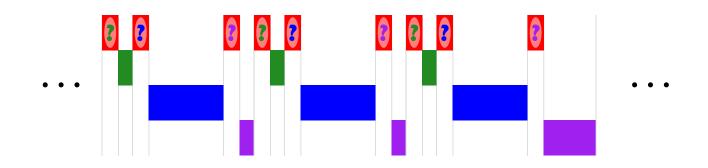
same idea, but for simultaneous frequencies

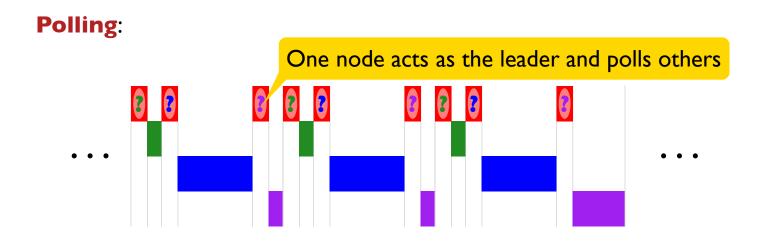


Time-division multiplexing (TDM):

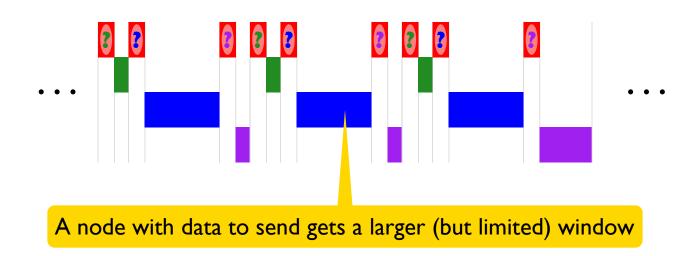
- + No collisions
- + Perfectly fair
- Poor utilization when some are idle

Polling:

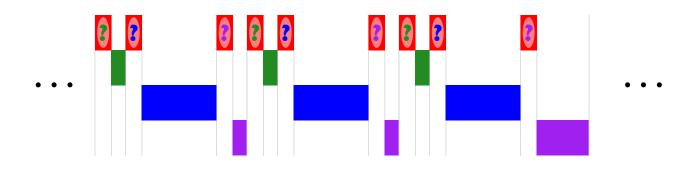




Polling:

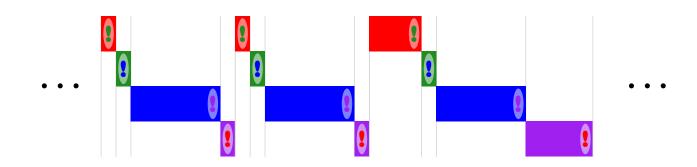


Polling:

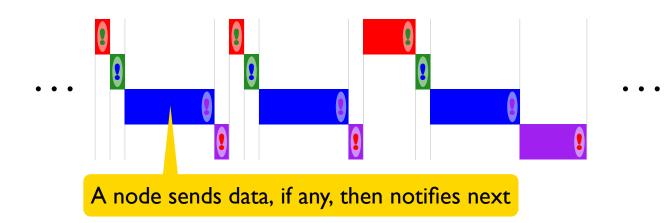


- + Better utilization
- Polling causes delays
- Recovery needed if the leader fails

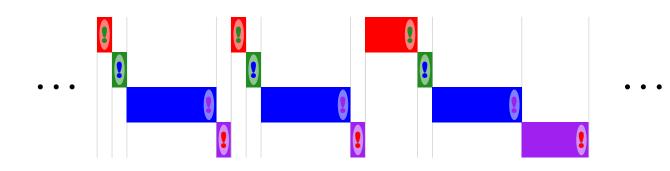
Token passing:



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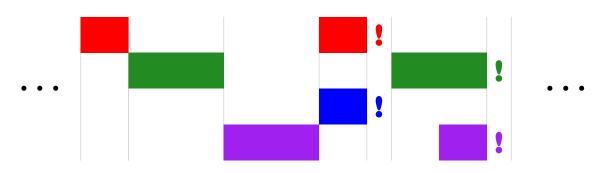
Token passing:



- + Better utilization
- Token-passing causes delays
- Recovery needed if any fails

Shared-Medium Strategy 3: Random Access

Random access:

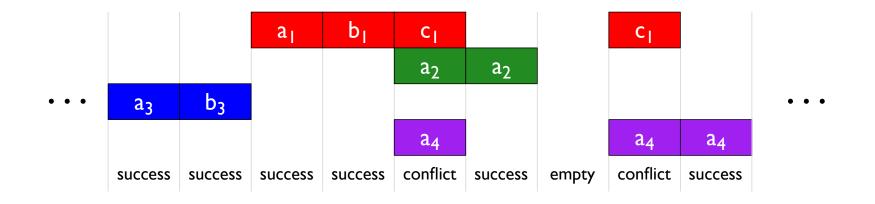


Need collision detection and/or carrier sense

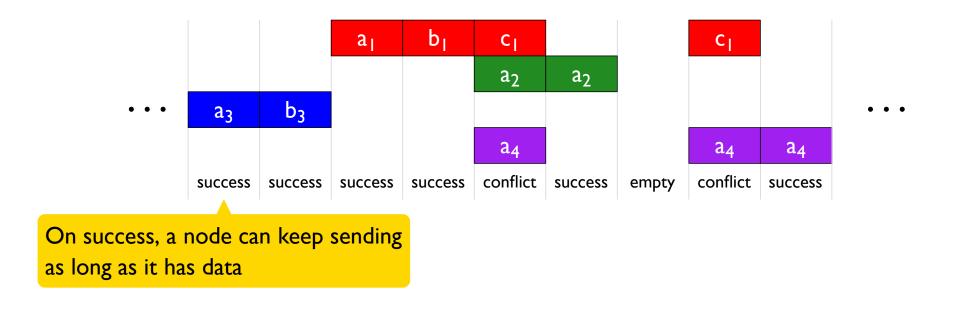
Random delay when collision is detected

+ Potentially better utilization

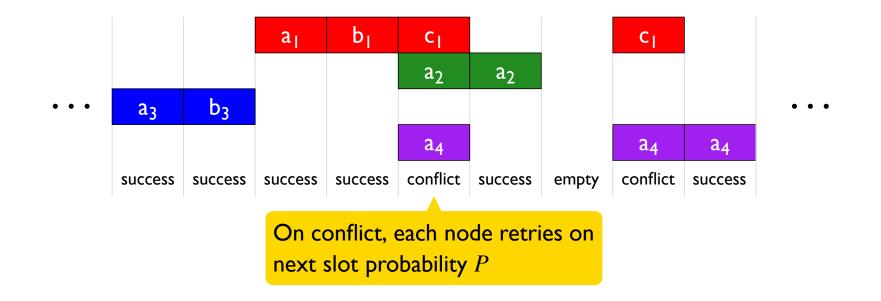
Random Access: ALOHA



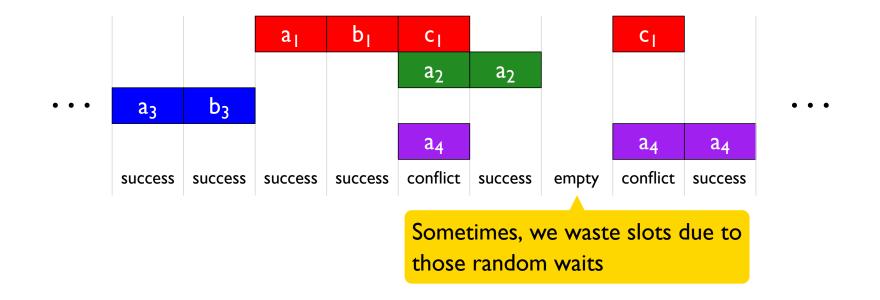
Random Access: ALOHA



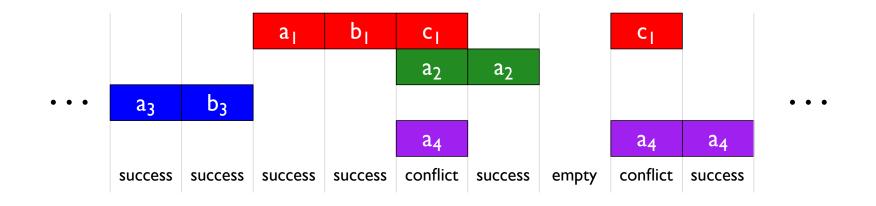
Random Access: ALOHA



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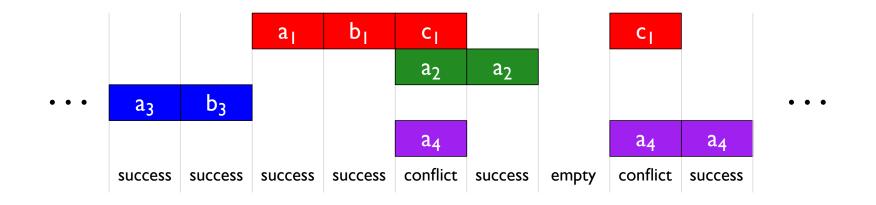
Random Access: ALOHA



Slotted ALOHA, which needs synchronization:

- + Sole active nodes can use full bandwidth
- + Multiple active nodes get fair share
- Even after optimizing P, likely to get only 37% success

Random Access: ALOHA



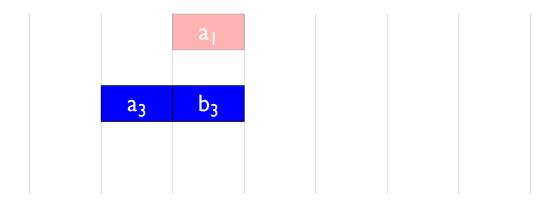
Original **unslotted ALOHA** avoided synchronization:

- Success drop drops by half

because each local slot likely overlaps two other peer slots

Carrier Sense Multiple Access (CSMA) means

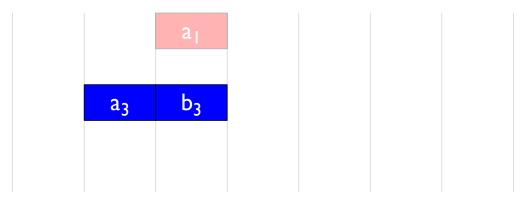
"don't talk when someone else is talking"



Carrier Sense Multiple Access (CSMA) means

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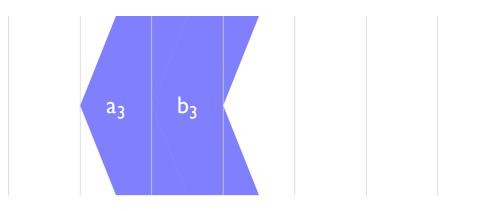
The catch: there's a delay between the time that one node sends and another node starts to sense it



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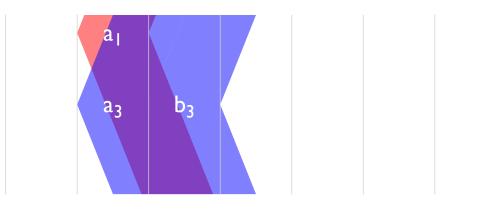
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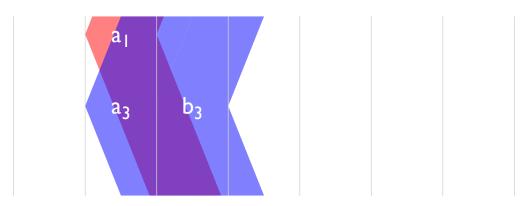


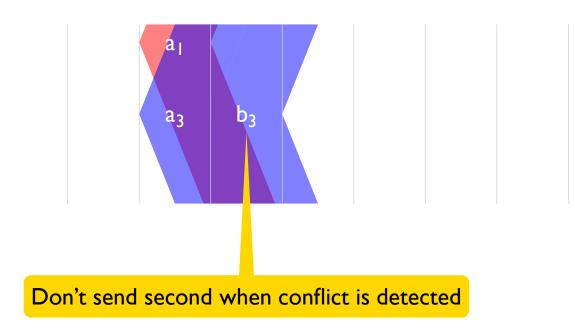
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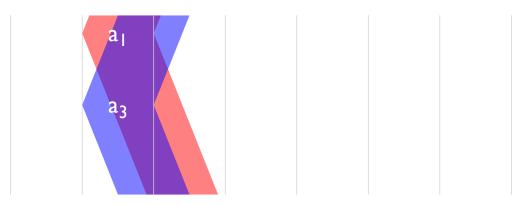
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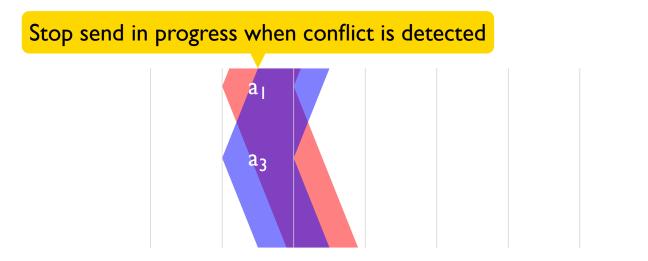
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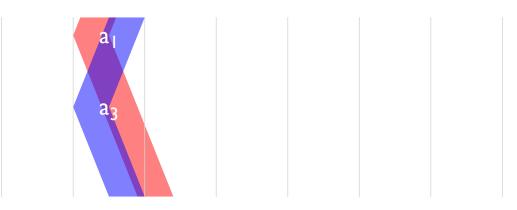


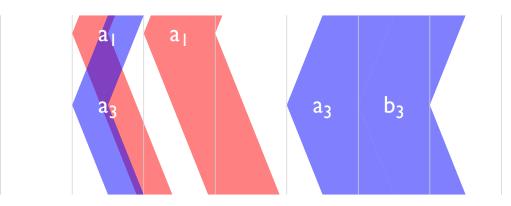


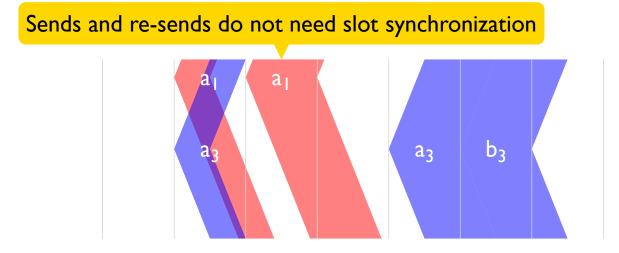
Some time need for conflict detection



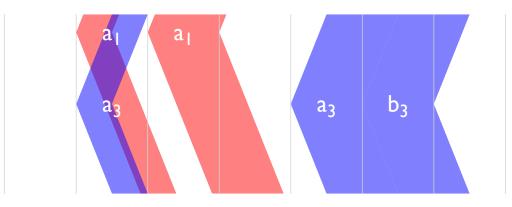
Random delay before retry

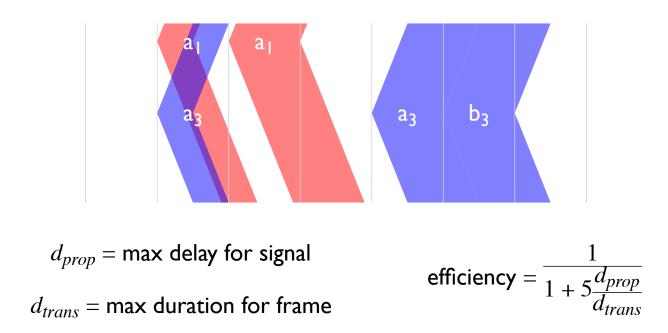


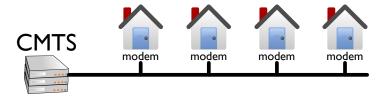


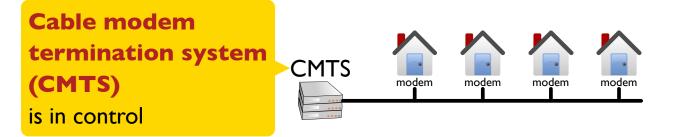


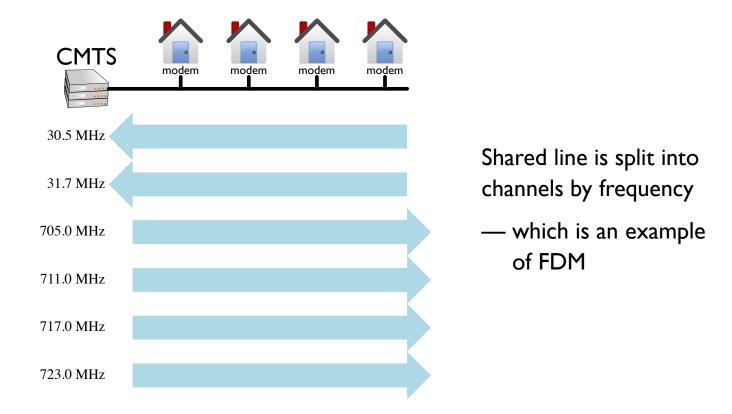
Exponential back-off: If another conflict, double average retry delay

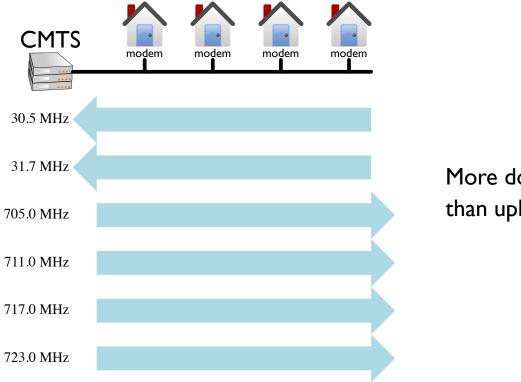




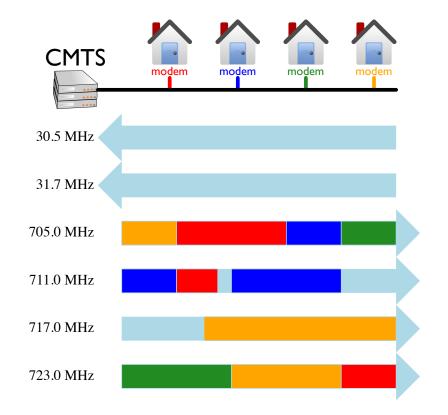






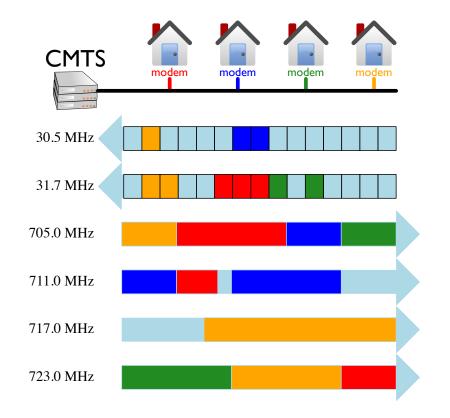


More download channels than upload channels

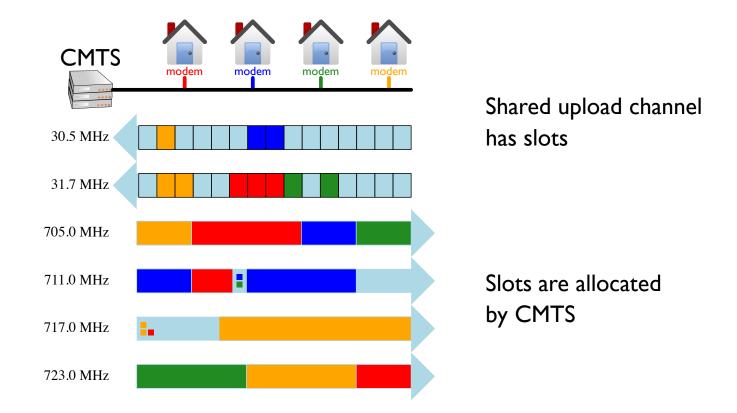


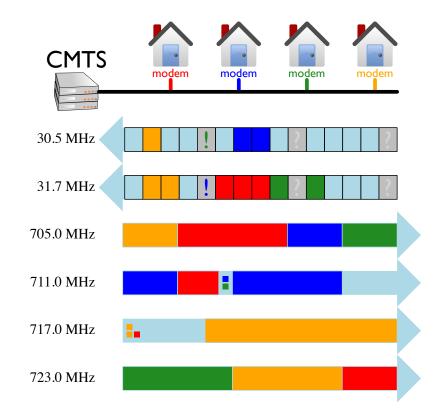
All modems see all download channels

Since only CMTS writes, no need for slots or collision handling



Shared upload channel has slots

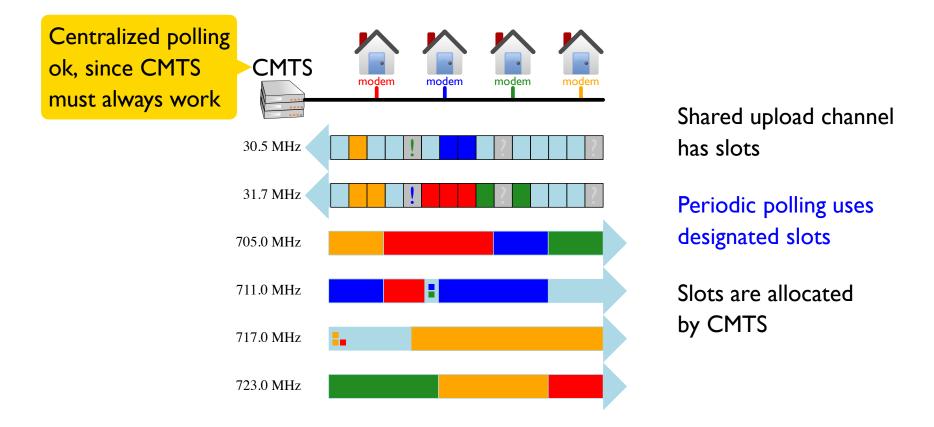


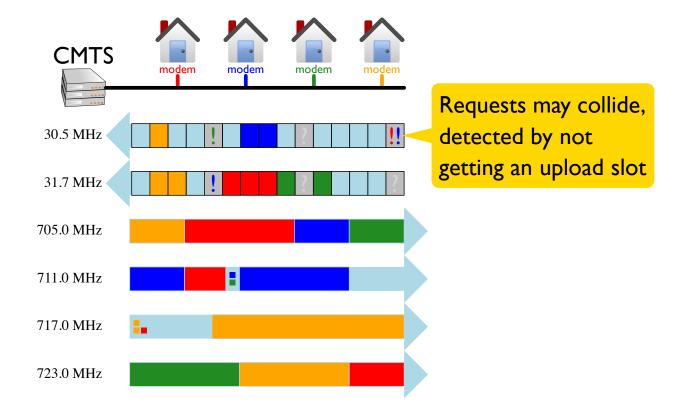


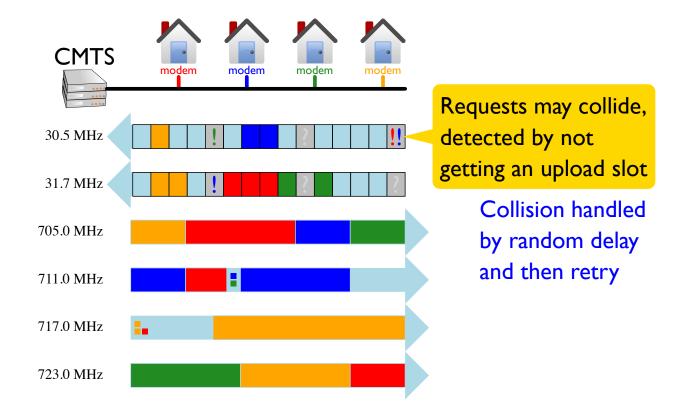
Shared upload channel has slots

Periodic polling uses designated slots

Slots are allocated by CMTS







Summary

Cyclic-redundancy check (CRC) commonly used at link layer

Link-to-physical transition often involves negotiating a shared medium

Two ways to share:

- Time-division multiplexing (TDM)
- Frequency-division multiplexing (FDM)

Three ways to allocate a division:

- polling
- token-passing
- random access
 - ... with **carrier sense** and/or collision detection

Layers — Done!

